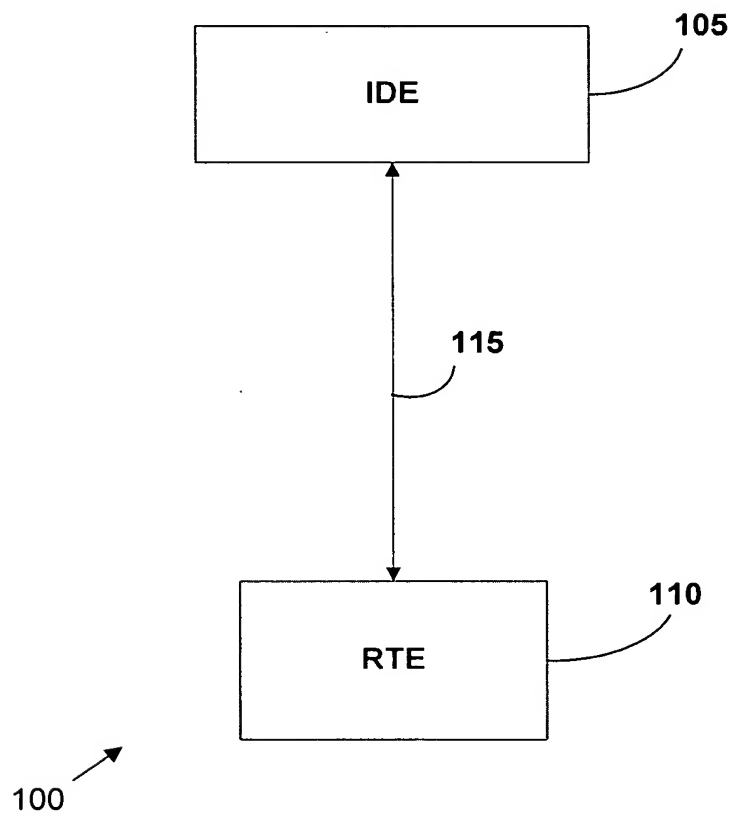
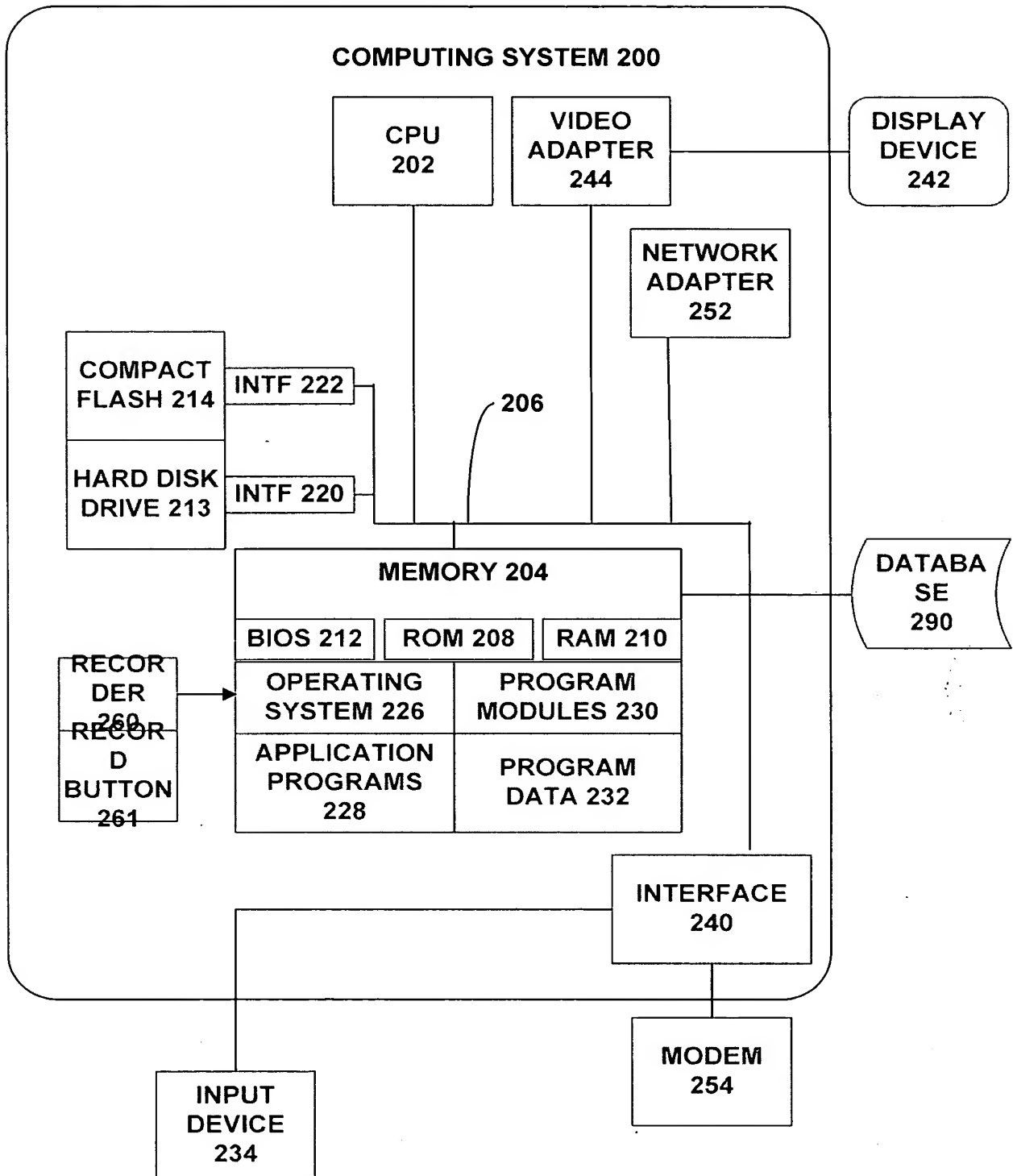


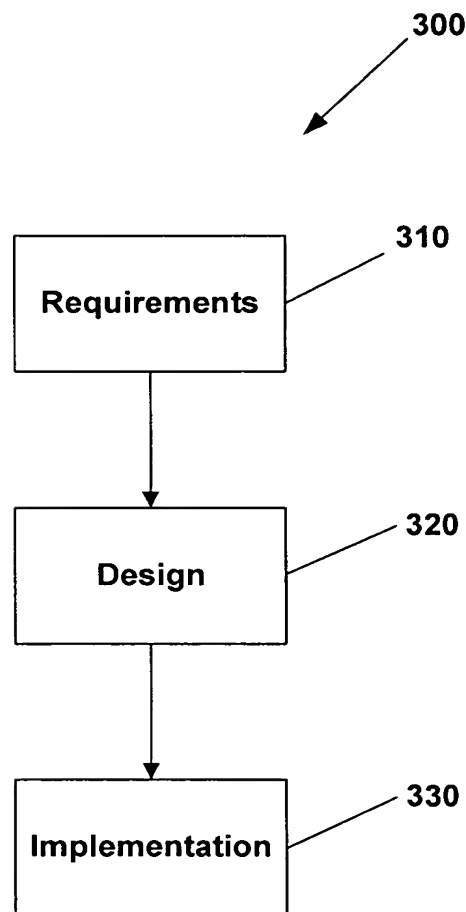
**Fig. 1**



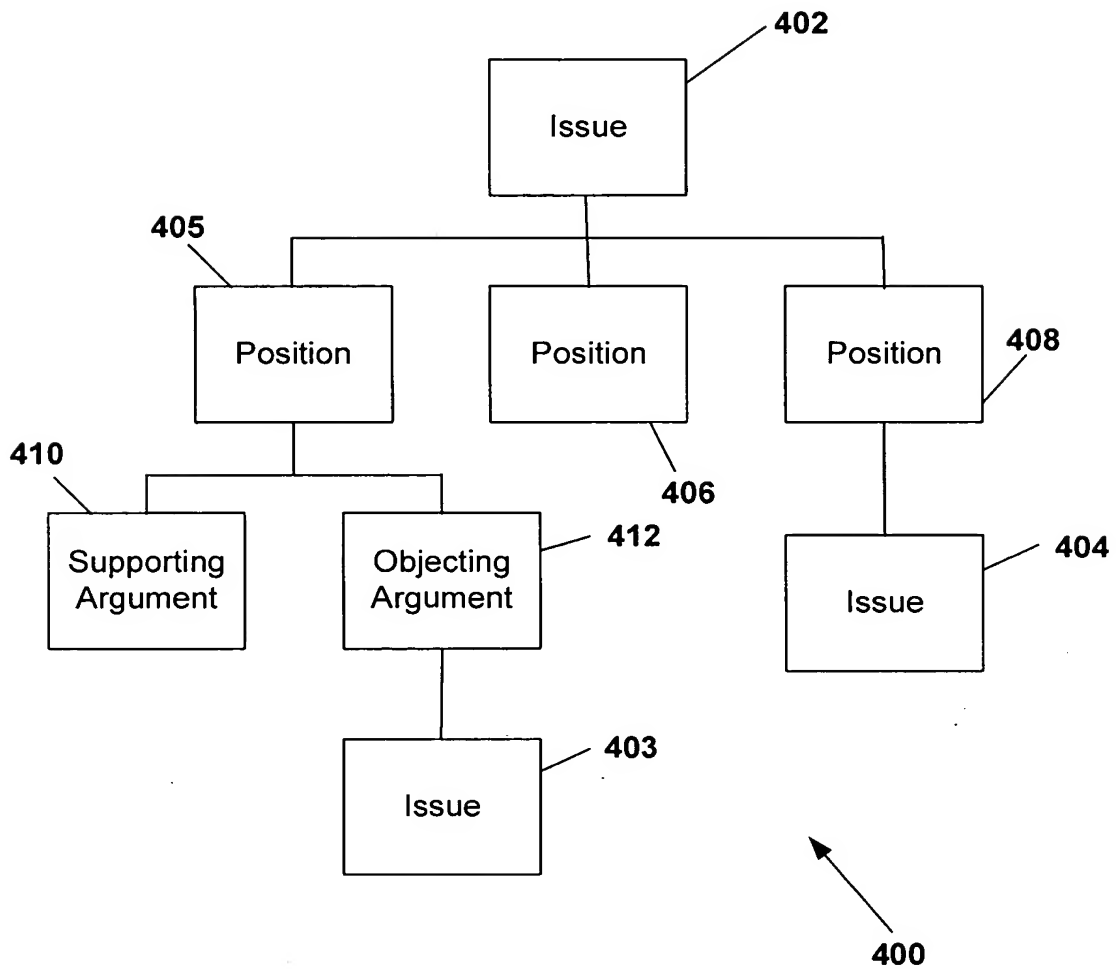
**FIG. 2**



**FIG. 3**



**FIG. 4**



**FIG. 5**

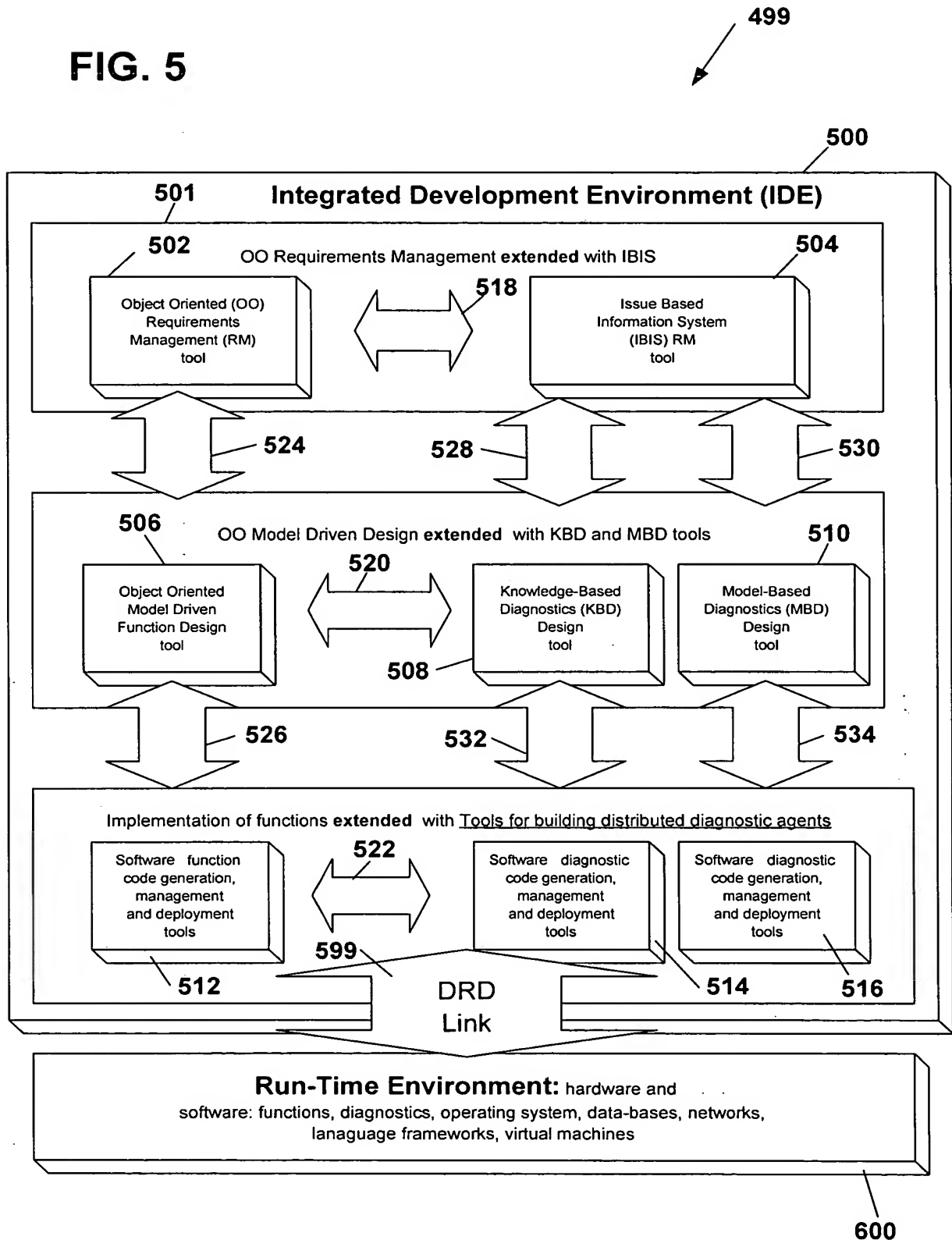


FIG. 6

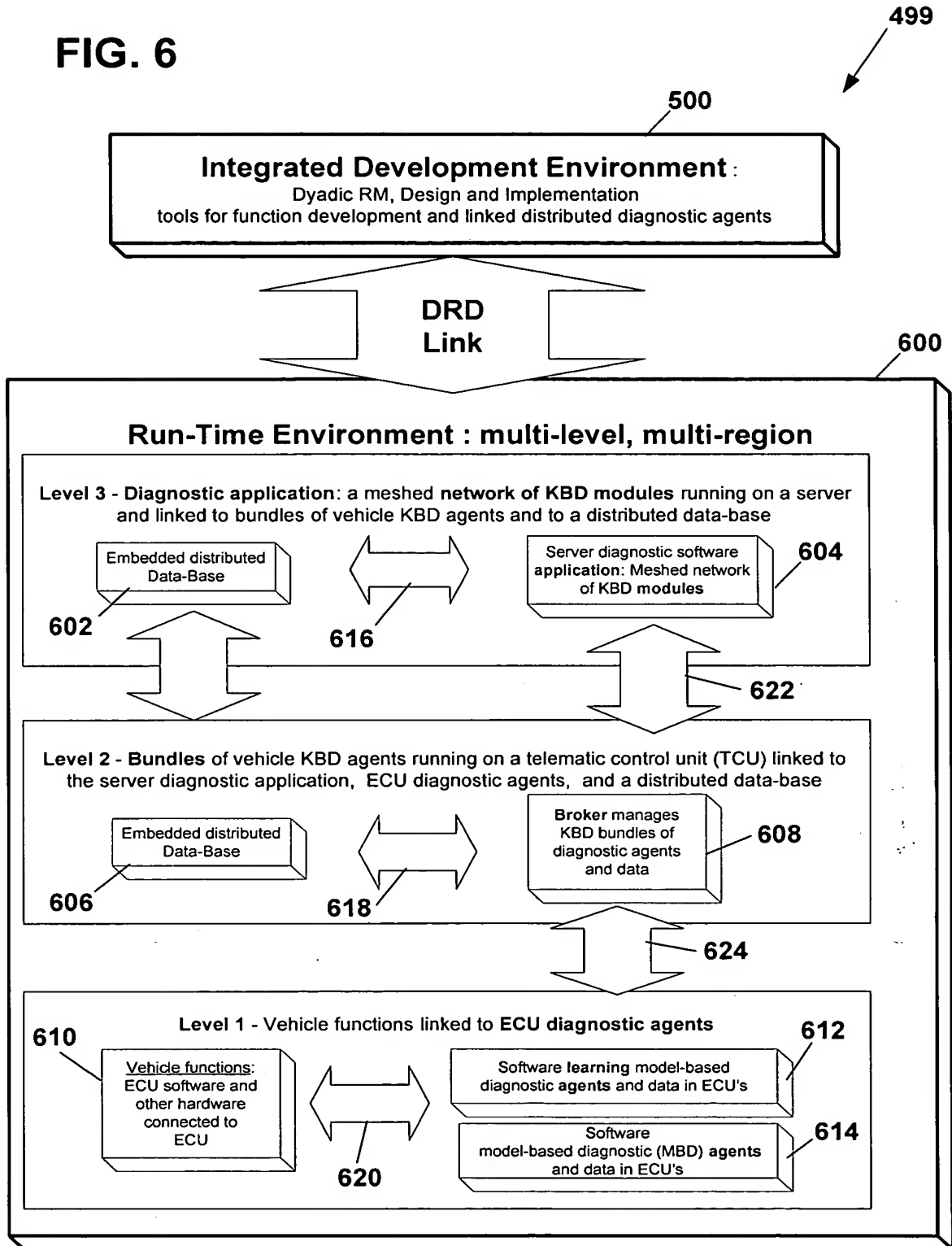
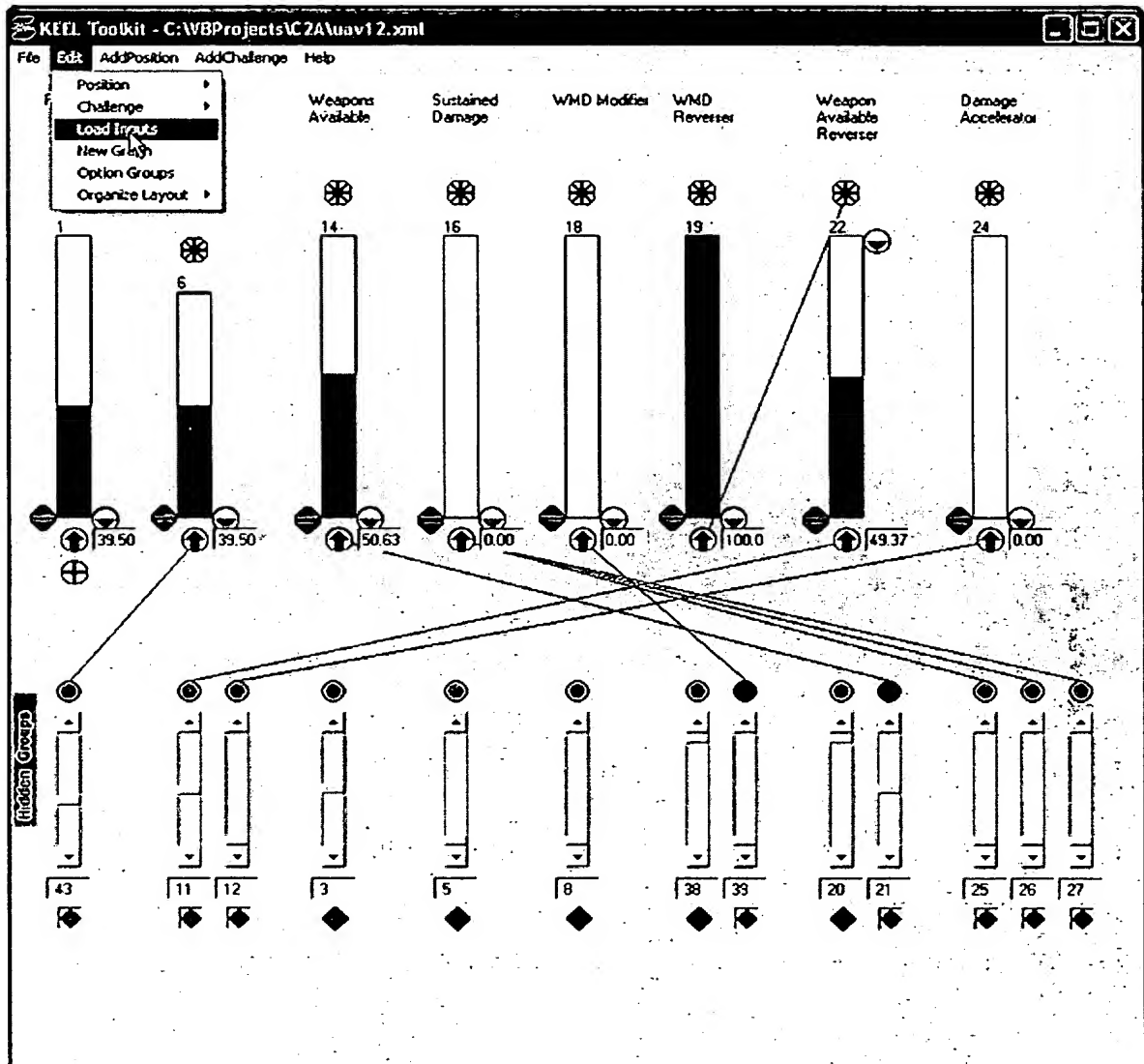
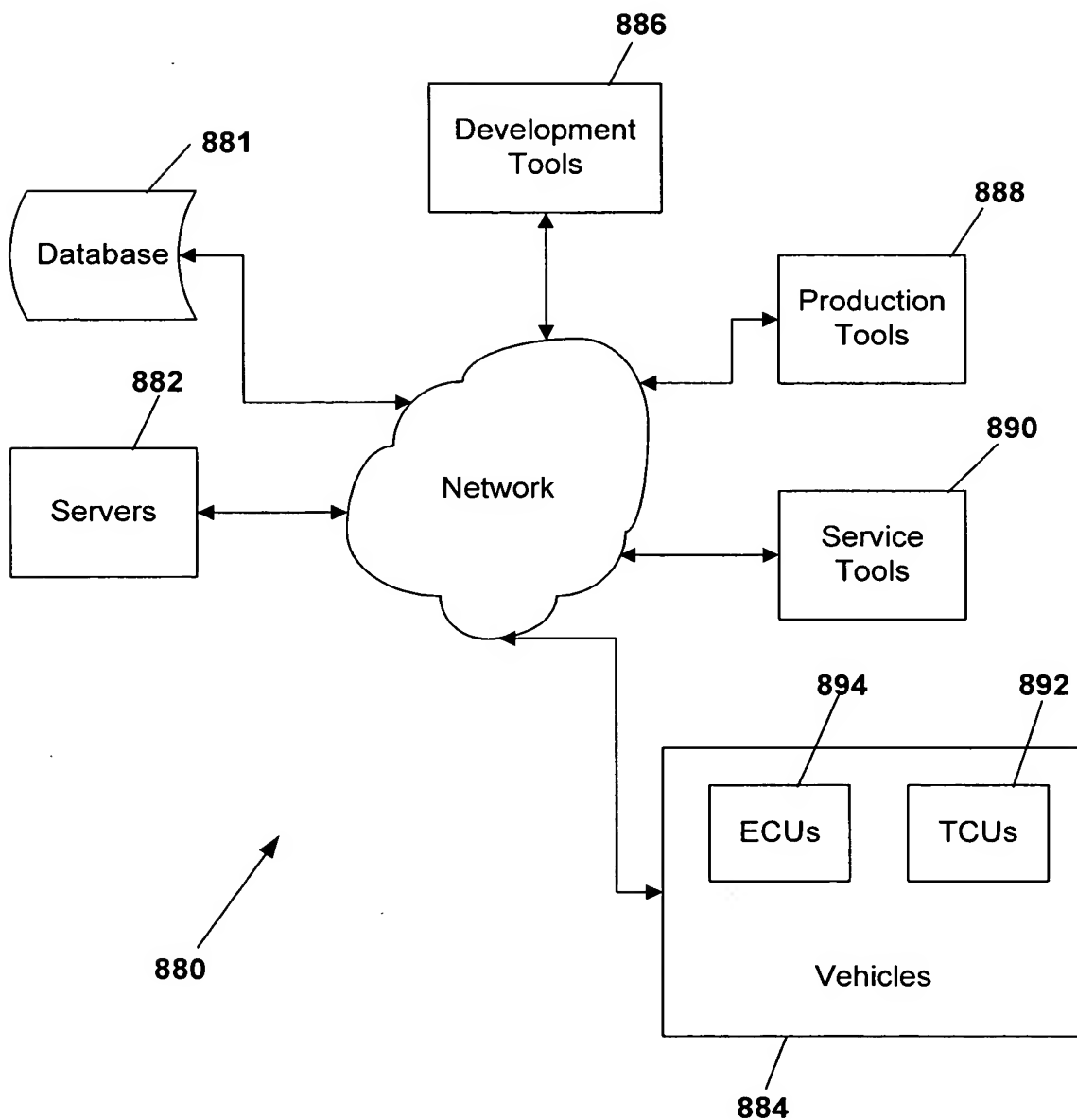


FIG. 7

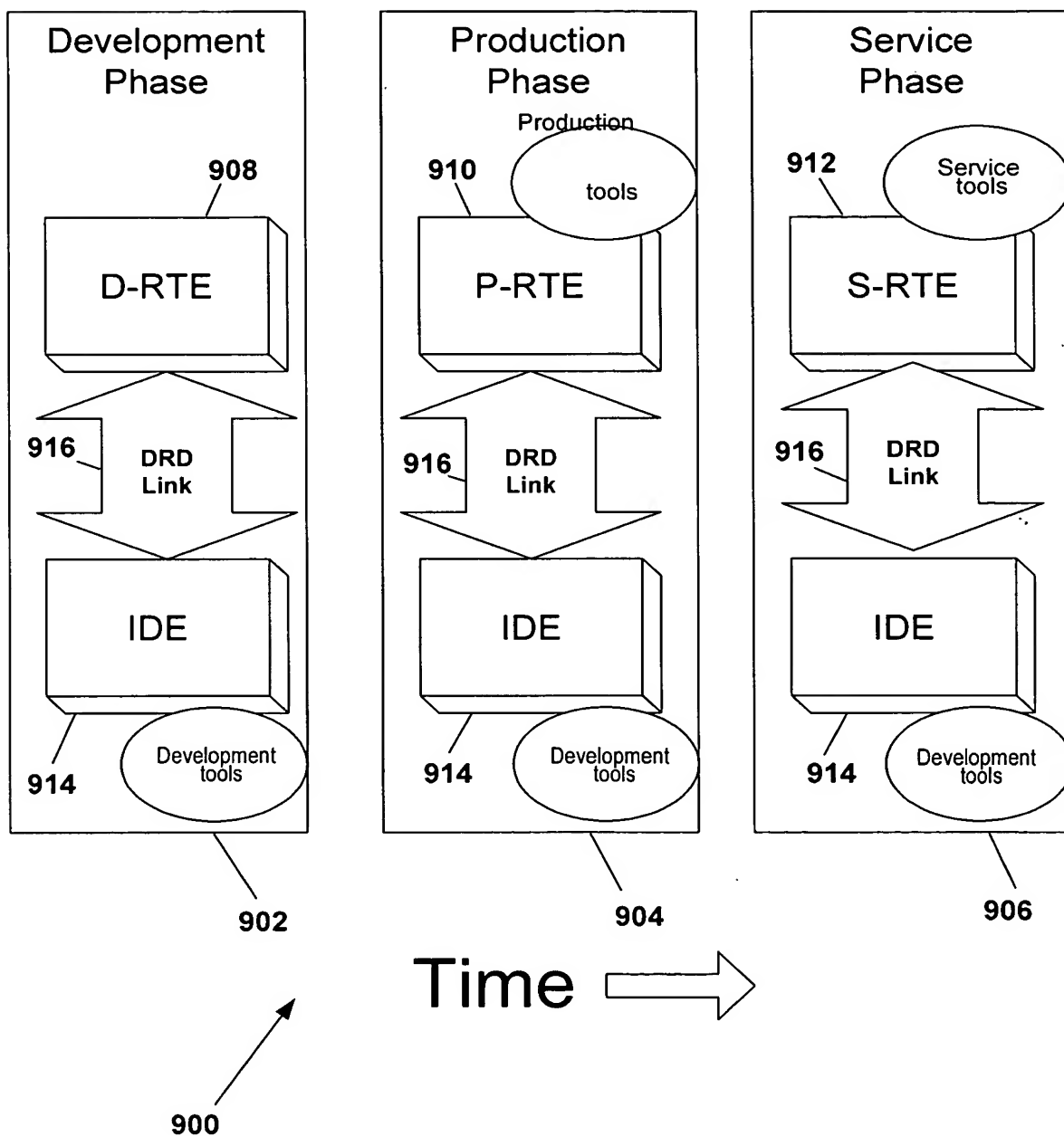


**FIG. 8**

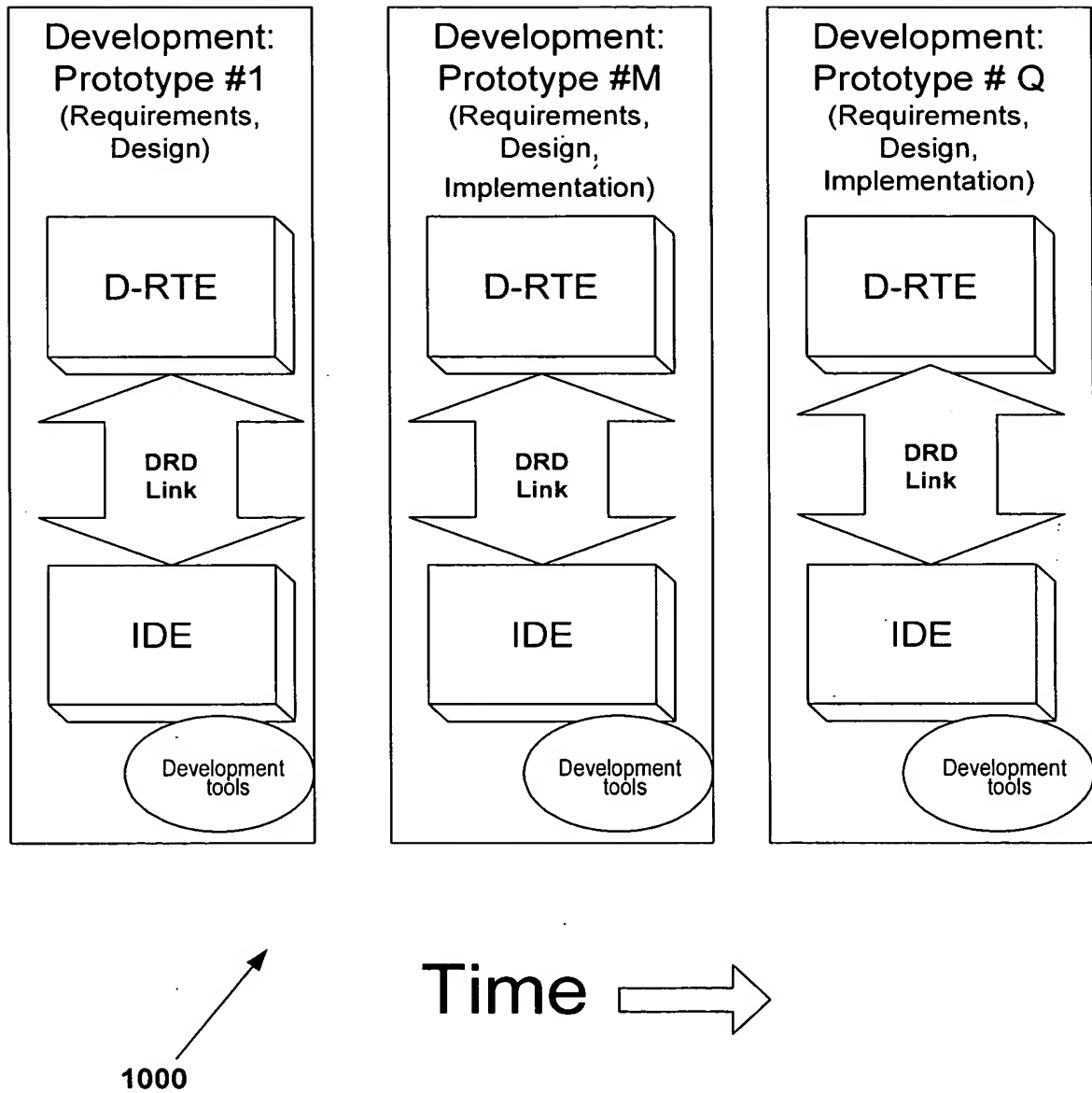




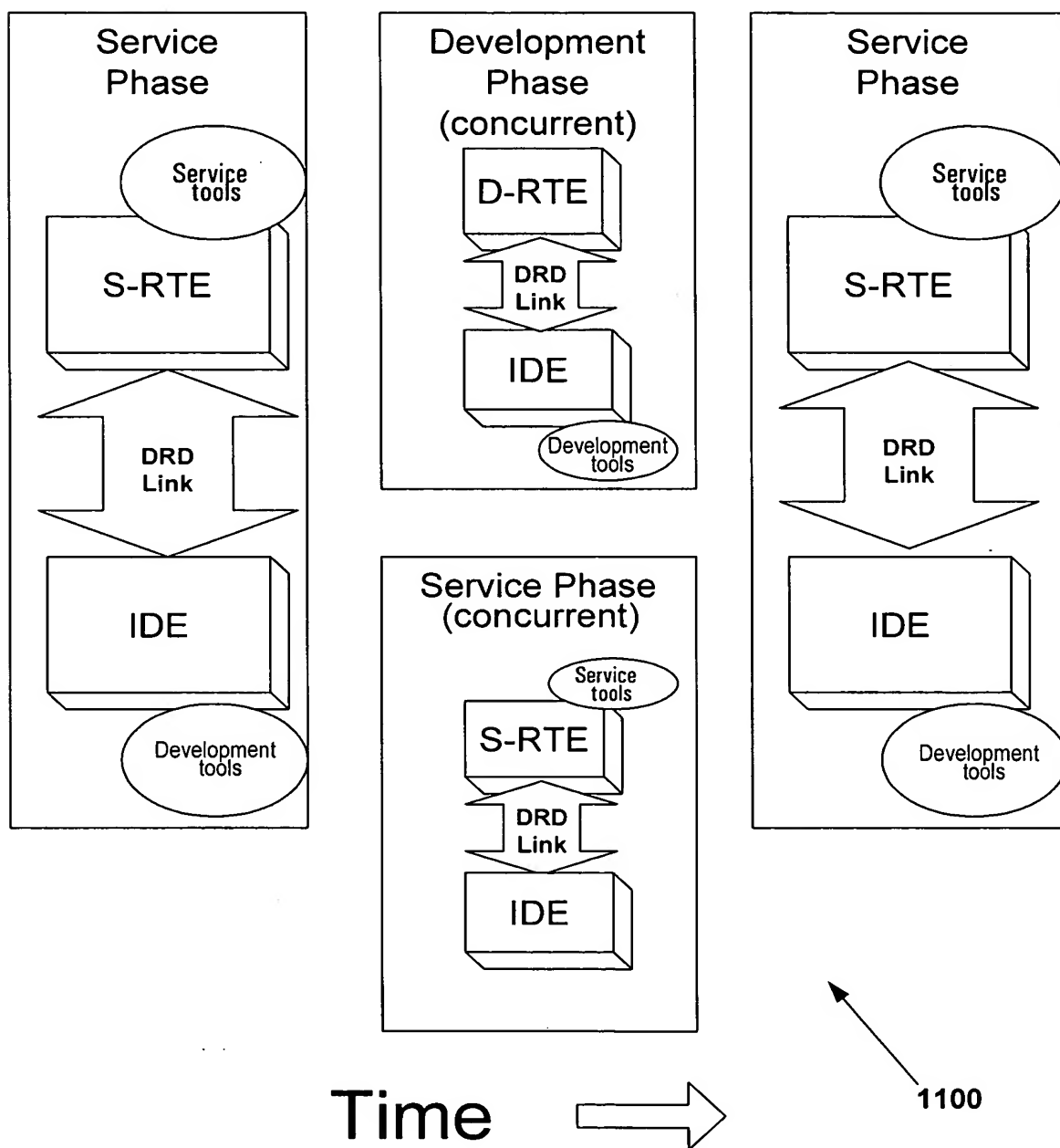
**FIG. 9**



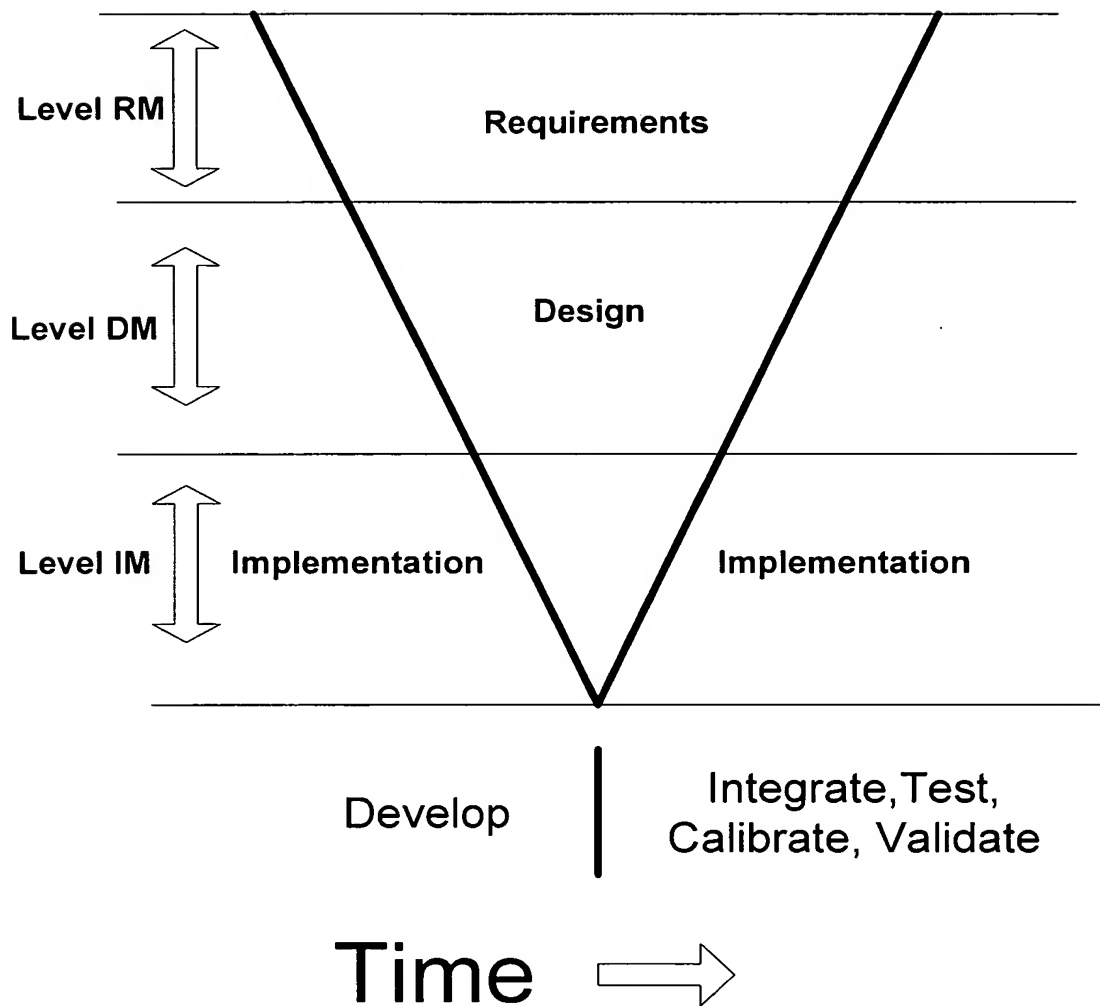
**FIG. 10**



**FIG. 11**

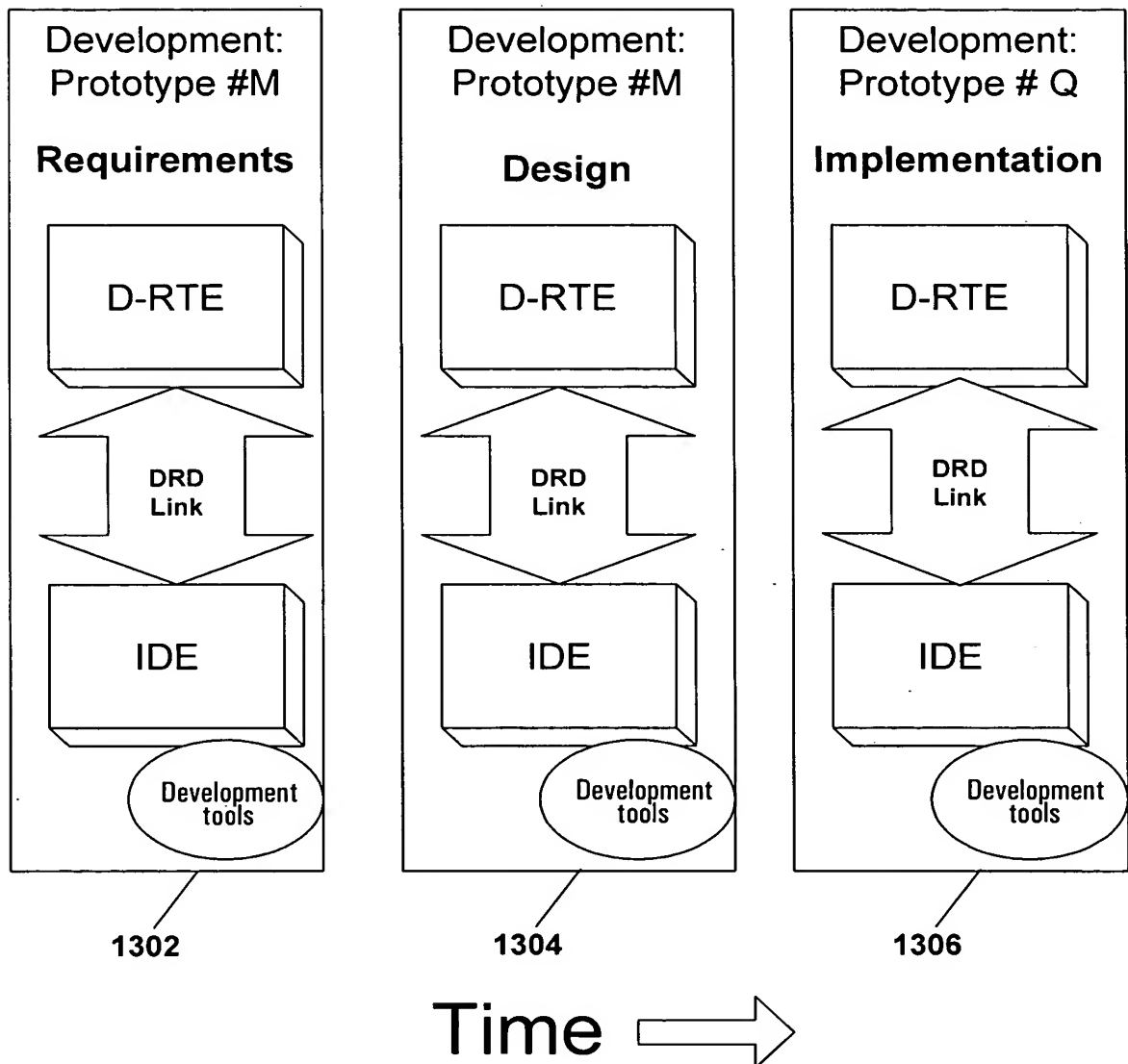


**FIG. 12**

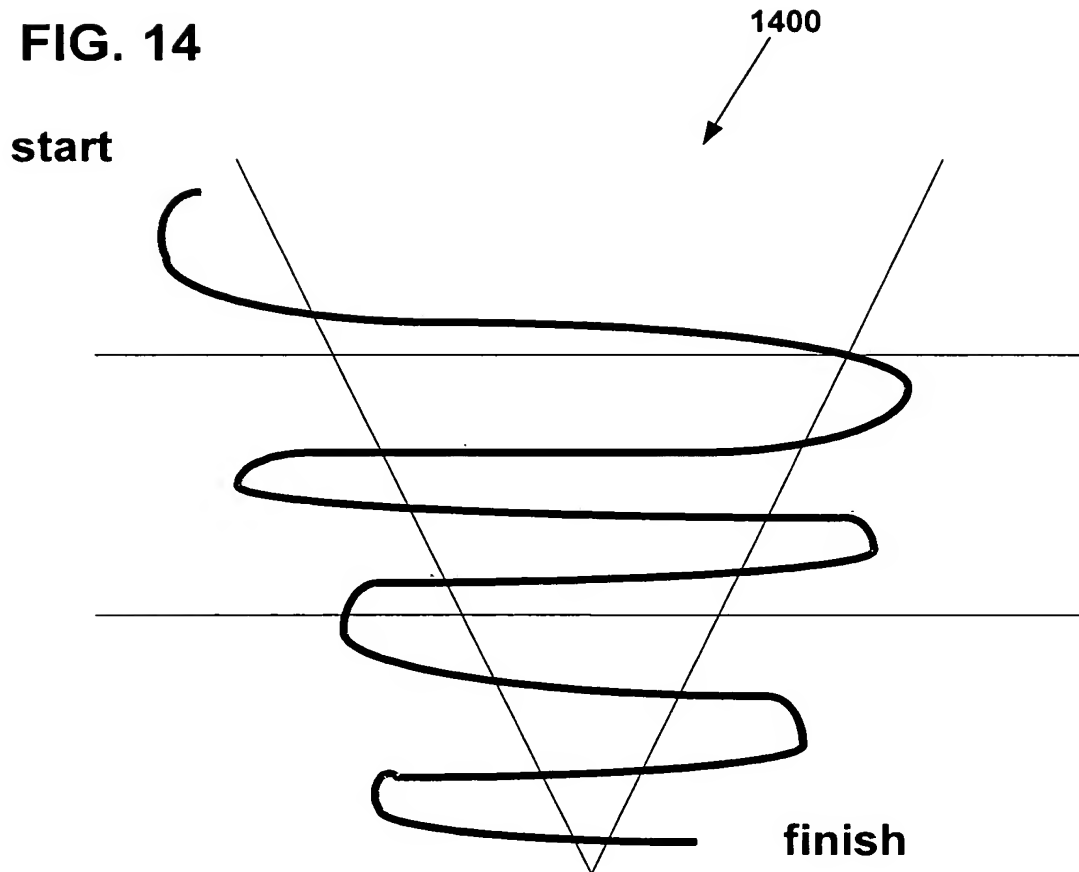


1200

**FIG. 13**



**FIG. 14**

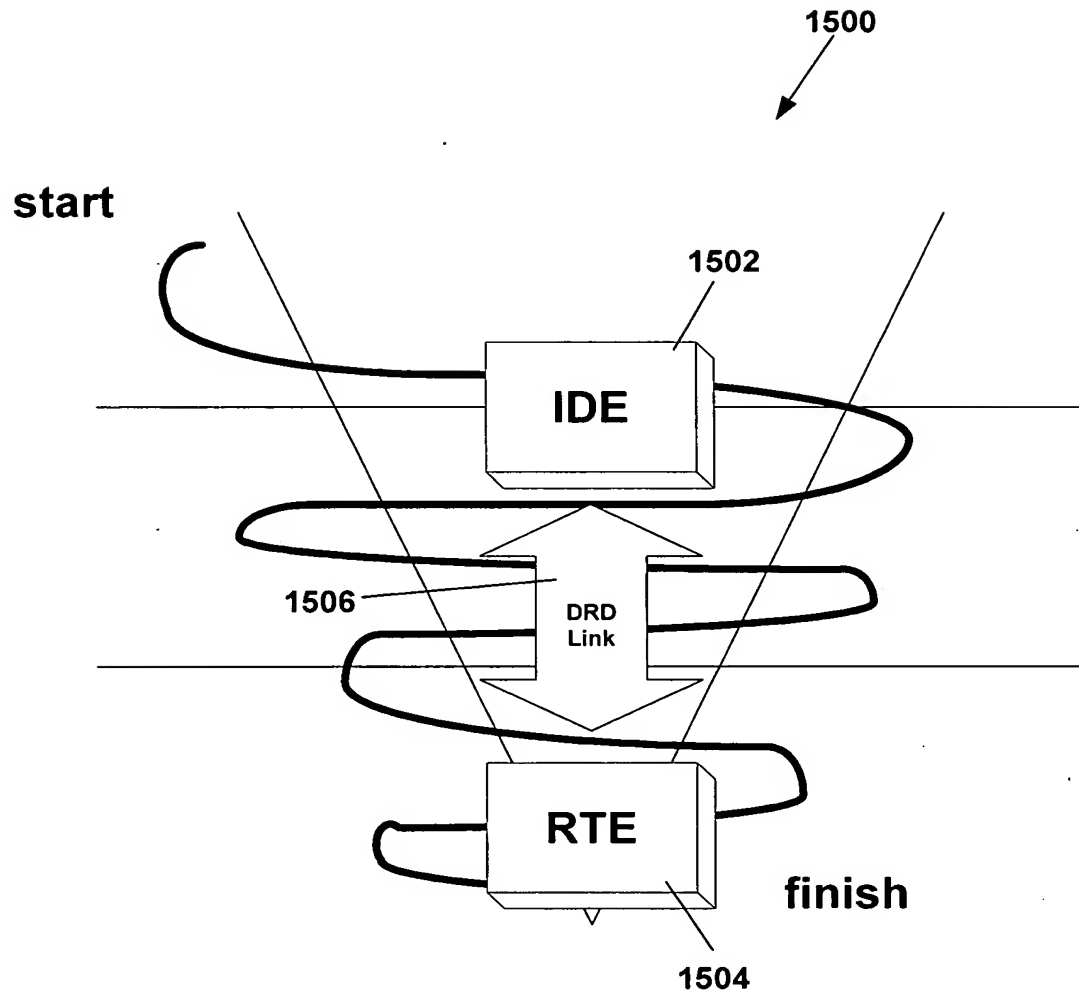


Time follows the spiral

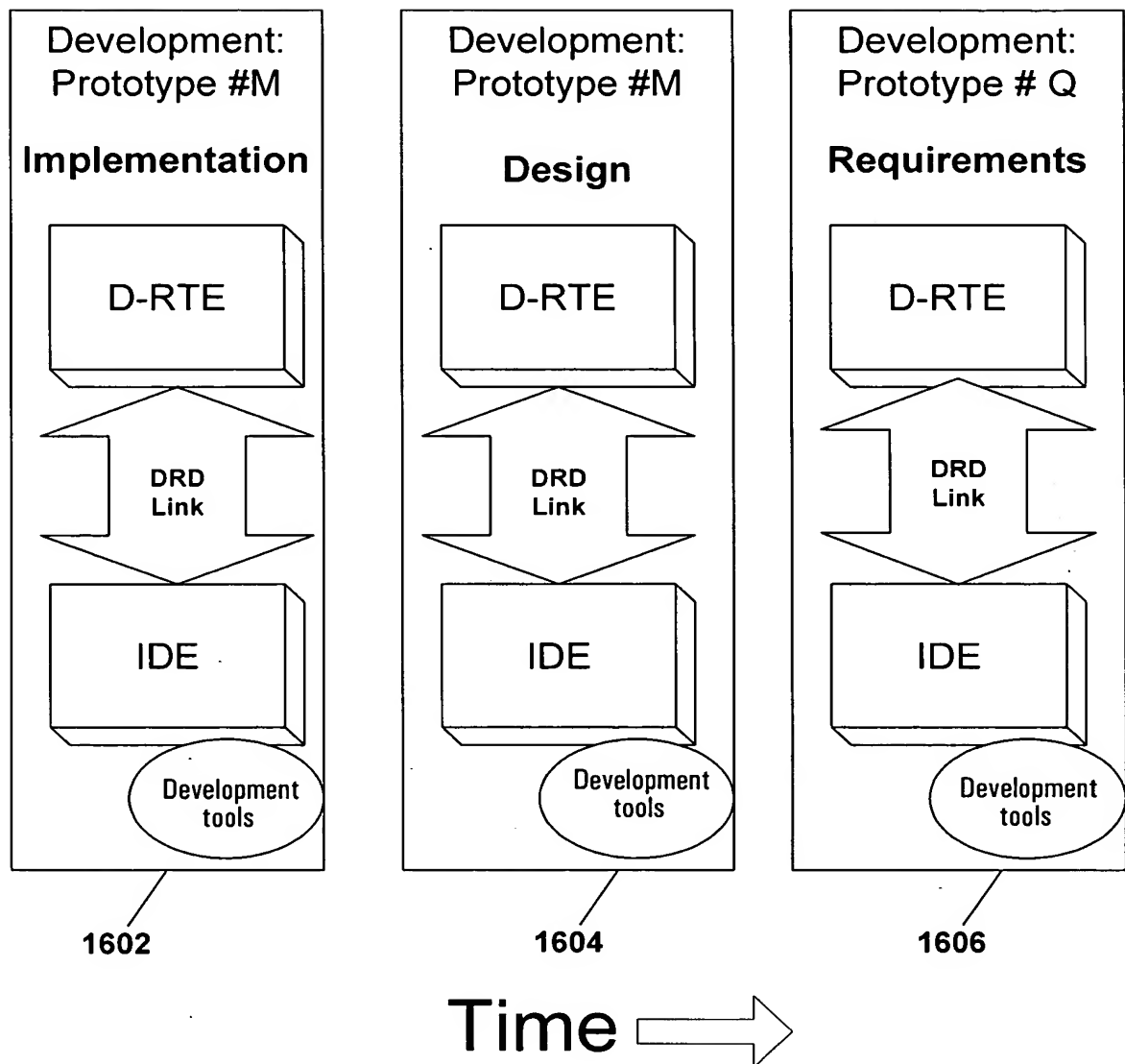
Develop

Integrate, Test,  
Calibrate, Validate

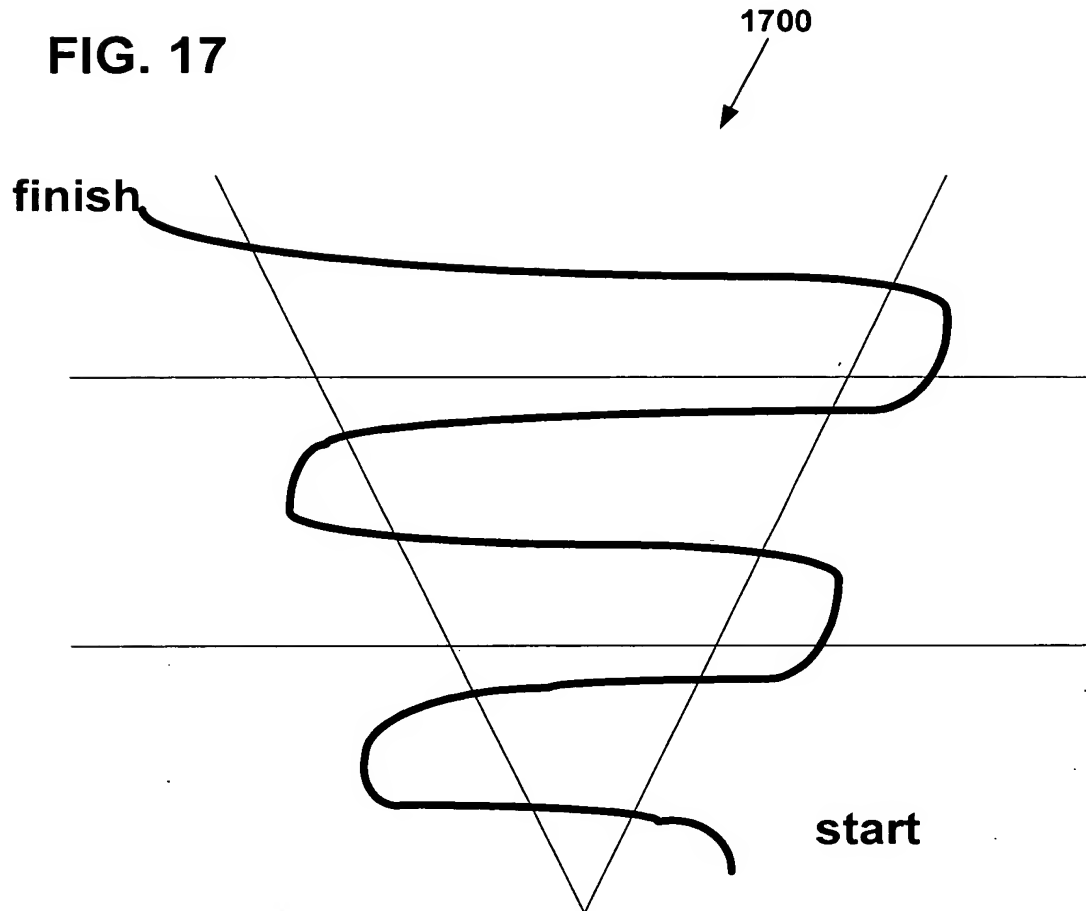
**FIG. 15**



**FIG. 16**





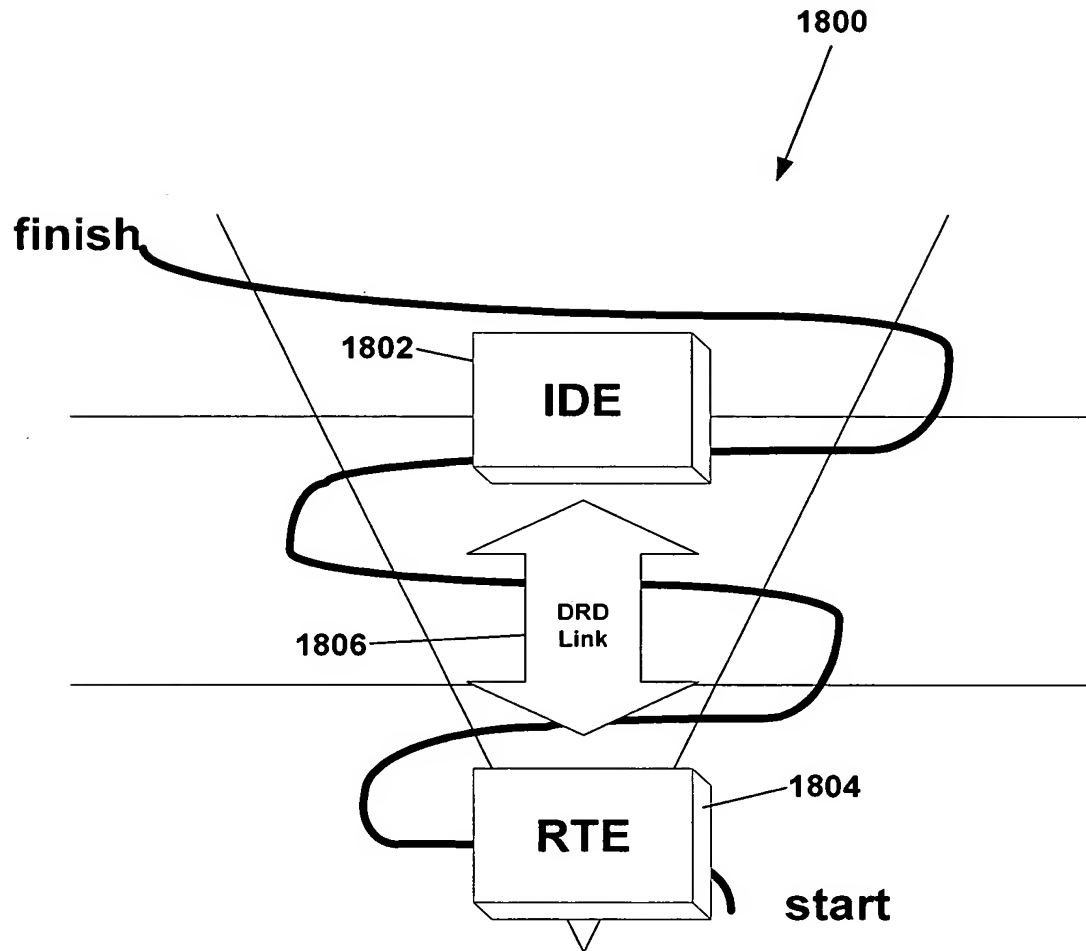


Time follows the spiral

Develop

Integrate, Test,  
Validate

**FIG. 18**



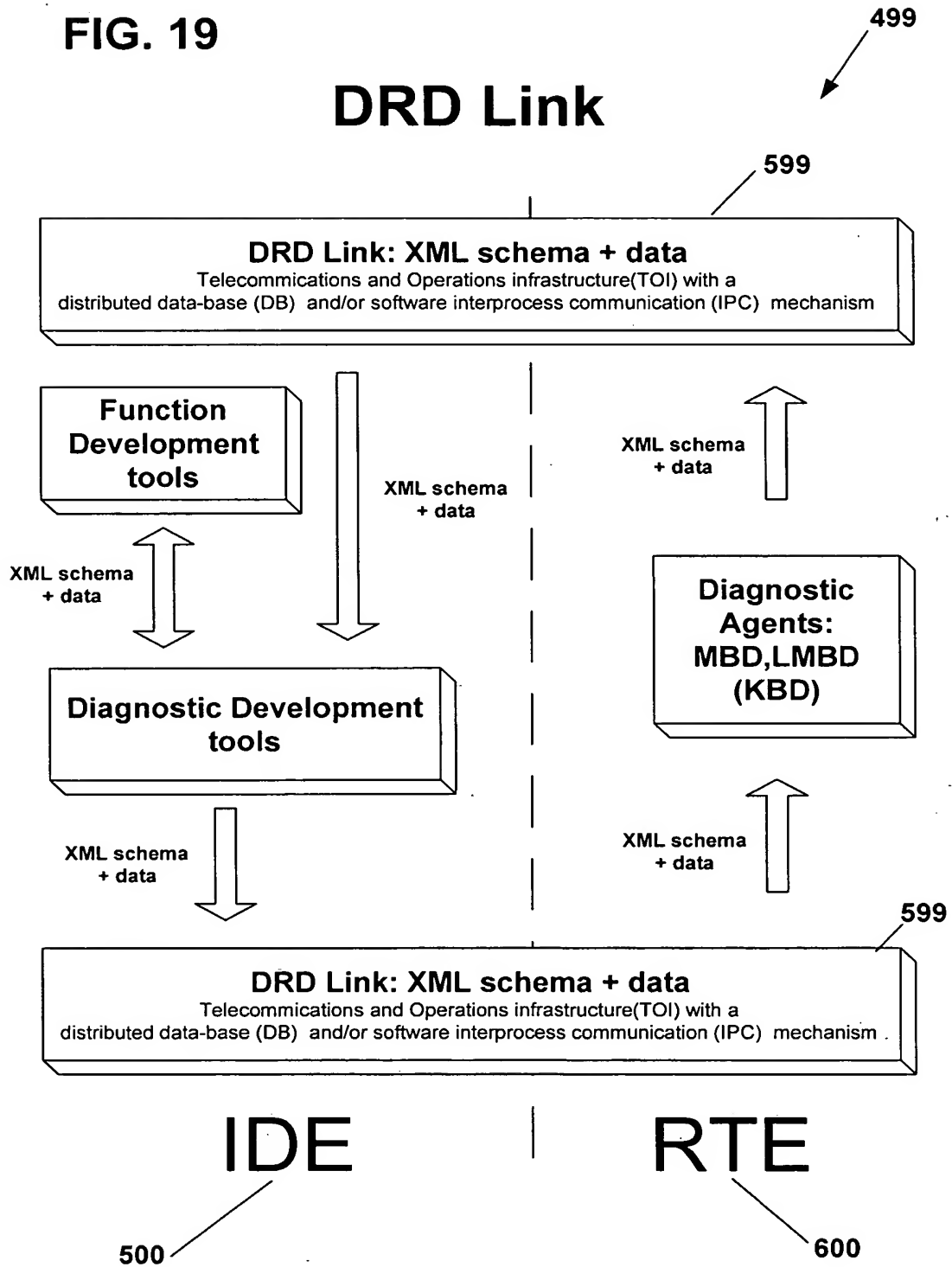
**Prototype "UP" cycle**

**Develop**

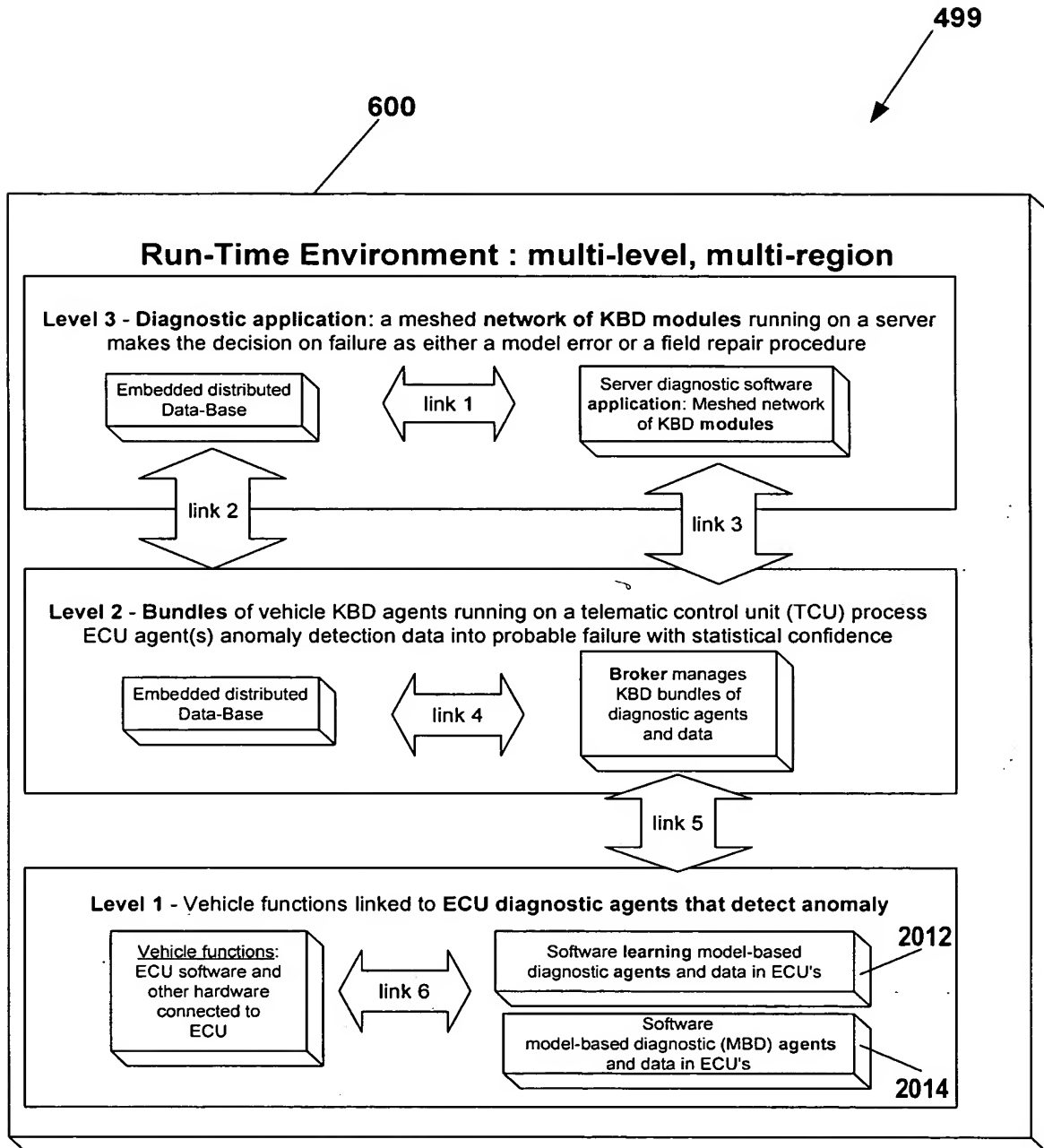
**Integrate, Test,  
Validate**

FIG. 19

## DRD Link



**FIG. 20**



**FIG. 21**

